

PANACEA IES/GATE INSTITUTE-DELHI

(The best institute for Electronics&Communication)
Under the guidance of Yogesh Agrawal&Team
(IES&GATE Topper with 7+ years teaching exp)



Strategy/Tricks/Books for IES

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What is IES for Electronics&Communication:

Indian Engineering Service exam is conducted by UPSC every year in 2nd week of June for post of class-1 officer in various departments like Indian Railway, Telecom, Defence, and Wireless&planning, Ordinance, powerengineering &inspection services etc. All posts will be as class-1 officer only.

Minimum qualification for IES Electronics job:

If you have done your B.TECH/BE in

1. Any branch which has word Electronics eg. Electronics&Telecomm, Electronics&comm&Electronics engineering.
2. Computer science/Information technology
3. Electrical engineering
4. Any branch related to instrumentation engineering

Note: If you belongs to any circuit branch (Electronics, computer, IT, Instrumentation&Electrical) you are eligible for IES in Electronics&Tele Communication. There is no need of minimum percentage in your degree

Pattern of paper:

There are 5 papers out of which 3 are objective types and 2 are conventional (Subjective) papers. Every paper carries 200 marks. These exams are conducted for 3 days. Result of written exam is declared and then successful candidates are called for interview which also carries 200 marks. So final result is declared out of 1200 marks.

Paper-1: 120 questions of GS&English (60+60)...Time-2hr

Paper-2: 120 questions of Electronics&Comm paper-1...Time-2hr

Paper-3: 120 questions of Electronics&Comm paper-2...Time-2hr

Paper-4: Theoretical&Numerical questions (Totally subjective paper)

It also carries 200 marks. All questions from Electronics&Comm...Time 3hr

Paper-5: Theoretical&Numerical questions (Totally subjective paper)

It also carries 200 marks. All questions from Electronics&Comm...Time 3hr

Negative marking: 1/3 mark will be deducted as penalty for every wrong question. There is no negative marking in conventional paper.

Standard of questions:

Questions asked in all 5 papers require depth and conceptual study of Subject. For solving all those questions you must have basic concept and good practice of objective questions.

No of posts:

Generally no of seats vary from year to year and it is in the range of 60-70 Out of which most of the posts belong to Indian Railway Service of Signal Engineering (IRSSE). Reservation is as per Government of India rule.

Minimum cut-off marks:

After written exam UPSC short list students on the basis of performance in total marks in all 3 objective papers. Conventional paper will be evaluated only of successful students in objective paper. But interview call will be on the basis of performance in total 5 papers.

For interview call cut-off for general category is 425-450 marks. For OBC, SC & ST it will be reduced accordingly.

Range of highest marks in IES-Electronics (written exam)

Highest mark in IES-E&T written vary in range of 560-600. Total highest marks vary in range of 700 (including interview marks).

1. Paper-1: In range of 100
2. Paper-2: in the range of 125
3. Paper-2 :in the range of 125
4. Paper-4: in the range of 150
5. Paper-5: in the range of 100

Note: These marks may vary from year to year but range remains same.

Syllabus for IES Electronics & Communication paper:

PAPER-1:

1. Materials and Components: Structure and properties of Electrical Engineering materials; Conductors, Semiconductors and Insulators, magnetic, Ferroelectric, Piezoelectric, Ceramic, Optical and Super-conducting materials. Passive components and characteristics Resistors, Capacitors and Inductors; Ferrites, Quartz crystal Ceramic resonators, Electromagnetic and Electromechanical components.

2. Physical Electronics, Electron Devices and ICs: Electrons and holes in semiconductors, Carrier Statistics, Mechanism of current flow in a semiconductor, Hall effect; Junction theory; Different types of diodes and their characteristics; Bipolar Junction transistor; Field effect transistors; Power switching devices like SCRs, GTOs, power MOSFETs; Basics of ICs - bipolar, MOS and CMOS types; basic of Opto Electronics.

3. Signals and Systems: Classification of signals and systems: System modelling in terms of differential and difference equations; State variable representation; Fourier series; Fourier transforms and their application to system analysis; Laplace transforms and their application to system analysis; Convolution and superposition integrals and their applications; Z-transforms and their applications to the analysis and characterisation of discrete time systems; Random signals and probability, Correlation functions; Spectral density; Response of linear system to random inputs.

4. Network theory: Network analysis techniques; Network theorems, transient response, steady state sinusoidal response; Network graphs and their applications in network analysis; Tellegen's theorem. Two port networks; Z, Y, h and transmission parameters. Combination of two ports, analysis of common two ports. Network functions : parts of network functions, obtaining a network function from a given part. Transmission criteria : delay and rise time, Elmore's and other definitions effect of cascading. Elements of network synthesis.

5. Electromagnetic Theory: Analysis of electrostatic and magnetostatic fields; Laplace's and Poisson's equations; Boundary value problems and their solutions; Maxwell's equations; application to wave propagation in bounded and unbounded media; Transmission lines : basic theory, standing waves, matching applications, microstrip lines; Basics of wave guides and resonators; Elements of antenna theory.

6. Electronic Measurements and instrumentation: Basic concepts, standards and error analysis; Measurements of basic electrical quantities and parameters; Electronic measuring instruments and their principles of working: analog and digital, comparison, characteristics, application. Transducers; Electronic measurements of non electrical quantities like temperature, pressure, humidity etc; basics of telemetry for industrial use.

Paper-2:

1. Analog Electronic Circuits: Transistor biasing and stabilization. Small signal analysis. Power amplifiers. Frequency response. Wide banding techniques. Feedback amplifiers. Tuned amplifiers. Oscillators. Rectifiers and power supplies. Op Amp, PLL, other linear integrated circuits and applications. Pulse shaping circuits and waveform generators.

2. Digital Electronic Circuits: Transistor as a switching element; Boolean algebra, simplification of Boolean functions, Karnaguh map and applications; IC Logic gates and their characteristics; IC logic families : DTL, TTL, ECL, NMOS, PMOS and CMOS gates and their comparison; Combinational logic Circuits; Half adder, Full adder; Digital comparator; Multiplexer Demulti-plexer; ROM an their applications. Flip flops. R-S, J-K, D and T flip-flops; Different types of counters and registers Waveform generators. A/D and D/A converters. Semiconductor memories.

3. Control Systems: Transient and steady state response of control systems; Effect of feedback on stability and sensitivity; Root locus techniques; Frequency response analysis. Concepts of gain and phase margins: Constant-M and Constant-N Nichol's Chart; Approximation of transient response from Constant-N Nichol's Chart; Approximation of transient response from closed loop frequency response; Design of Control Systems, Compensators; Industrial controllers.

4. Communication Systems: Basic information theory; Modulation and detection in analogue and digital systems; Sampling and data reconstructions; Quantization & coding; Time division and frequency division multiplexing; Equalization; Optical Communication: in free space & fibre optic; Propagation of signals at HF, VHF, UHF and microwave frequency; Satellite Communication.

5. Microwave Engineering: Microwave Tubes and solid state devices, Microwave generation and amplifiers, Waveguides and other Microwave Components and Circuits, Micro strip circuits, Microwave Antennas, Microwave

Measurements, Masers, lasers; Microwave propagation. Microwave Communication Systems terrestrial and Satellite based.

6. Computer engineering: Number Systems. Data representation; Programming; Elements of a high level programming language PASCAL/C; Use of basic data structures; Fundamentals of computer architecture; Processor design; Control unit design; Memory organisation, I/o System Organisation. Microprocessors: Architecture and instruction set of Microprocessors 8085 and 8086, Assembly language Programming. Microprocessor based system design: typical examples. Personal computers and their typical uses.

Strategy for Electornics&communication objective papers:

For solving objective papers student must have very good concept in electronics&communication engineering. The best formula for cracking objective papers in IES is to solve last 15-20 years objective papers of IES/GATE Electronics&Communication .It is not possible to solve questions by a single book. So try to build concept and solve objective questions

How we at panacea can help for Electronics&Comm objective section:

Most of the questions in objective papers of electronics&communication come from our class notes only. Our classes are taken by IES&GATE toppers and they teach in class what they studied for themselves. It's very difficult to get correct solution of last 15 years questions in market. We solve all last 15-20 years questions in our class-room with full explanation. We provide exact solution of all last 15-20 years questions. Other than these questions we also provide Probable 500-600 extra questions on each topic. We also conduct topic test every week during class room program. Other than this we also conduct test series 2-3 month before the IES exam.

Pattern of conventional papers:

Paper-1: This paper carries 200 marks. In this paper 7 questions are asked in which each question carries 40 marks. Every question has 2-3 parts. So on an average every question carry 15 marks.

General pattern of paper-1 is as follows:

Q.1(A)&1(B) Material science-15x2=30

Q.1(c) EDC-10 marks

Q.2(A),2(B)&2(C) Electronic device&circuit-15x2+10=40

Q.3(A)&3(B)-Signal system-15x2=30

Q.3(C)-Signal&system-10 marks

Q.4(A),4(B)&4(C)-Network theory-15x2+10=40

Q.5(A)&5(B)-Network theory-15x2=30

Q.5(c)-EMT-10 marks

Q.6(A),6(B)&6(C)-EMT-15x2+10=40

Q.7(A),7(B)&7(C)-Instrumentation-15x2+10=40

Note: out of these 7 questions one has to attempt only 5 questions.

Paper-2: This paper has 3 sections.

Section-A:

This section contains 10 questions and every question carries 8 marks from whole syllabus of paper-2

Section-B:

This section contains Analog, Digital&control system. This contains 4 questions in which each question carries 30 marks. Out of 4 you have to attempt only 2 questions. Every question has 3 parts carrying 10 marks.

Section-C:

This section contains Communication, Microwave&Microprocessor/Computer engineering. This contains 4 questions in which each carries 30 marks. Out of 4 you have to attempt only 2 questions. Every question has 3 parts carrying 10 marks.

Strategy for Electornics&communication conventional papers:

For solving conventional papers student must have very good concept in electronics&communication engineering. The best formula for cracking subjective papers in IES is to solve last 15-20 years conventional papers of IES Electronics&Communication papers. It is not possible to solve questions by a single book. So try to build concept and solve subjective questions.

How we at panacea can help for Electronics&Comm conventional section:

Most of the questions &derivations in subjective papers of electronics&communication come from our class notes only. Our classes are taken by IES&GATE toppers and they teach in class what they studied for themselves. It's very difficult to get correct solution of last 15 years questions in market. We solve all last 15-20 years questions only in our class-room with full explanation. We provide exact solution of all last 15 years questions. We also provide probable questions for conventional papers.

Note:

In conventional paper accuracy is very important and students must concentrate on answer only. Especially in paper-2 there is no step marking so one has to be very cautious there and must avoid solving wrong questions.

Strategy& Books for Electronics&Communication:

Paper-1:

1. Material&Component:

It is theoretical subject and questions asked are theoretical in nature. There are 20 questions in objective paper and 40 mark questions in conventional paper-1. Type of questions asked are information based. If you don't have proper material¬es then this subject becomes very difficult.

Reference books of material&component:

1. Material science&engineering by V.Raghavan
2. Electrical material by C.S. Indulkar
3. Solid state physics by Pillai
4. Electrical material by S.P.Seth&Deccar
5. Solid-State Electronics by Streetman
6. Integrated Electronics by Milliman&Helkias

Strategy at Panacea Institute for Material&Component:

Since last 5-6 years most of the questions are coming from our class notes. Our class notes include relevant information from above given books. These class notes&study material is prepared on the basis of last 20 years questions. Some relevant information is also taken from some international journals. In conventional paper questions are direct and it is very easy to handle them. We provide around 500-600 questions as assignment. For conventional questions we are also giving many additional questions.

2. Electronics&device circuit:

This subject requires depth study of knowledge and one must have good concepts for solving questions. 20 questions are asked in objective paper&60 marks questions in conventional paper. In this subject questions are mixture of information and concept.

Reference Books for Electronics&device circuit:

1. Solid-State Electronics by Streetman
2. Integrated Electronics by Milliman&Helkias
3. Micro-Electronics Circuit by Sedra-Smith
4. Electronics-Device &Circuit Theory by Boylested
5. Linear Integrated Electronics by D.R.chaudhary
6. Op-Amp & Linear I.C. by Gayakwad
7. Pulse, Digital & Switching by Milliman & Taub
8. Electronics-Principal by Malvino

Strategy at Panacea Institute for Electronics&device circuit:

In this portion we give more stress on concept as well tricks for solving objective problems. Since last 5-6 years most of the concepts in exam are based upon our class notes. Our class notes and study material are based upon the concept&information given in above books and last 15-20 years IES/GATE. In class we give around 500-600 questions on this topic as assignment and 300-400 questions in class itself. For IES&GATE exam we are also providing some additional questions for this topic. For conventional paper we are giving additional questions.

3. Electronics&Instrumentaion:

This subject is theoretical and numerical asked are direct and formula based. In this subject syllabus is too vast and one has to be very much selective while studying this subject. 20 questions are asked in objective paper and 40 marks questions in subjective paper. In paper-1 out of 7 you have to attempt 5 questions so you can study this subject from objective point of view only but then you have to very much confident in remaining subjects.

Reference Books for Electronics&Instrumentation:

1. Electrical and Electronics Measurement by A.K.Sahni.
2. Electronics Measurement by Cooper
3. Electronics Measurement by Kalsi

Strategy at Panacea Institute for Electronics&Instrumentation:

Classes of E&I are taken by IES in Electrical engineering because this subject is related to electrical engineering. Our class notes are on the the basis of Concept&information given in above book. Most of the questions in exam are on the basis of class notes only. We provide 500-600 practice questions in class as

assignment and do many questions in class itself. We are also providing many additional questions for this topic.

4. Electromagnetic theory:

This subject requires depth study of knowledge and one must have good concepts for solving questions. In this subject some questions are information based also. 20 questions are asked in objective paper & 60 marks questions in conventional paper. In this subject questions are mixture of information and concept. In this subject most of the questions in exam come after boundary condition only. Very few questions are asked from Electrostatic & Magnetostatic portion.

Reference books for Electromagnetic theory:

1. Elements of Electromagnetic by N.O.Sadiku
2. Electromagnetic Theory by K.D.Prasad
3. Electromagnetic waves and Radiation by J.D.Kraus
4. Electromagnetic by Haytt.
5. Electromagnetic Waves and Radiation by Jordan & Balman.

Strategy at Panacea Institute for Electromagnetic theory:

We discuss all concepts and information based upon above given books and last 20 years questions. We give more stress to transmission line, waveguide & antenna as in EMT most of the questions come from this portion only. Type of antenna, waveguide their patterns are very conceptual and require some tricks for solving problems. In this portion we give around 500-600 problems as assignment and do many problems in class itself. Additional 300-400 questions are also given. This portion is asked in both papers and so requires special attention for this subject. We give many probable conventional questions as assignment.

5. Signal & System and Random variable:

It is totally numerical subject and questions asked are totally numerical. For solving these questions some tricks are required which come from practice only. So need of doing more conceptual problems in this section. 20 objective questions and 40 marks questions are asked in conventional paper. Without signal system you can not understand concepts in communication engineering. Random variable is very conceptual but questions asked are very direct.

Reference books for signal system and Random variable:

1. Signal System by Openheim & Willsky
2. Signal & System by M.J.Roberts
3. Problems from Schaum-Series
4. Random Variable from Peebles
5. Random Variable topic from B.P.Lathi

Strategy at Panacea Institute for Signal-System & Random variable:

Very few institute discuss random variable in detail due to lack of availability of material & teacher. We discuss this topic in very detail and do many questions on this topic. We also discuss many tricks & questions on random variable. For solving questions of signal & system we tell many short cut methods and tricks. 500-600 questions are given as assignment. We discuss many objective & subjective problems in class room itself. 300-400 probable questions are also given for IES

separately. Most of the questions & concepts of signal & system and random variable come from our class notes only.

6. Network theory:

It is totally numerical subject and questions asked are totally numerical in nature. For solving these questions some tricks are required which come from practice only. So need of doing more conceptual problems in this section. 20 objective questions are asked in network theory and 80 marks questions are asked in conventional paper.

Reference Books:

1. Circuit-Theory by A.Chakrabarti
2. Circuit-Theory by Soni-Gupta
3. Network Analysis by Valkenberg
4. Schaum-Series Problems

Strategy at Panacea Institute for Network Theory:

Class notes, information & concepts is based upon above given books and last 15-20 years questions. This is totally mathematical subject and using tricks only one can solve most of the questions. Most of the questions in Network are solved by going through options by putting different values and then try to justify from your values. So for this topic magic mantra is developing tricks for objective problems. We work on that concept in class and develop question solving tricks for this topic. We provide 500-600 problems as assignment and 300-400 additional questions for IES. We also do many additional problems for conventional paper.

Paper-2:

1. Analog Electronics:

This is very much conceptual subject and questions asked are mixture of concept & understanding. Many numerical are asked but they require less calculation and more concept. For understanding this portion Electronics Device and circuit portion must be very clear to you. Many problems are asked from OP-AMP portion. 20 questions from objective and around 50 marks questions in conventional paper come from this portion.

Reference books are same as that of Electronics Device & Circuit

1. Solid-State Electronics by Streetman
2. Integrated Electronics by Milliman & Helkias
3. Micro-Electronics Circuit by Sedra-Smith
4. Electronics-Device & Circuit Theory by Boylsted
5. Linear Integrated Electronics by D.R.chaudhary
6. Op-Amp & Linear I.C. by Gayakwad
7. Pulse, Digital & Switching by Milliman & Taub
8. Electronics-Principal By Malvino

Strategy at Panacea for Analog electronics:

For electronics engineer EDC&Analog are very-2 crucial subjects because these portions come in both paper paper-1&paper-2.We give special emphasis for both EDC&Analog. We teach in very detail from concept point of view and most of the questions in this portion come directly from our class notes only.we do around 500-600 conceptual questions in this portion and do addition 300-400 questions for both theory and numerical point of view.

2. Digital electronics:

This is considered as easy subject and requires less depth study in this subject. Here questions asked are direct but for some portions like logic family,DAC/ADC,FFs&Counters you must have good concept of Transistor&OP-Amp.20 objective questions&50 marks questions in conventional paper come from this portion.

Reference Books:

1. Digital Logic& Computer design by Moris-Mano
2. Digital Integrated Electronics by Taub &Schilling
3. Digital Fundamentals by Flyod
4. Digital Principles by Malvino&Leach
5. Modern Digital Electronics by R.P.Jain

Strategy at Panacea for Digital:

Our class notes information and concepts are based upon above books and last 20 years GATE&IES questions. we give tricks for solving problems of K-Map,Counters,SHR,DAC.ADC&logic families. By one trick only you can solve any problem of counter. We do around 500-600 questions as assignment and many problems in class itself. We also give 300-400 probable problems for IES exam.

3. Control system:

This subject is similar to Network theory from study point of view but in this subject you have to understand theoretical part also. eg you must know theoretical concept of Nyquist and Root-locus. 20 objective questions and 50 marks conventional questions are asked. Root Locus, BodePlot, Nyquist-plot, State-variable, controller&GM/PM are main topics which require depth study.

Reference Books:

1. Modern Control Engineering by K.Ogata
2. Control System by B.C.Kuo
3. Control System Engineering by Nagrath&Gopal
4. Linear Control System by B.S.Manke
5. Problems in Control System by A.K.Jairath

Strategy at Panacea for Control system:

Our class notes, information&concepts are based upon above books &last 20 years GATE&IES questions. In this topic questions are mixture of both theory&numerical. Most of the questions in this portion come from our class notes only.we discuss tricks for solving problems of GM/PM, Nyquist, Polar, Bode, controller etcs.we discuss around 500-600 questions as assignment and 300-400 questions in class itself. Many probable questions are also given for IES exam.

Note:Analog,Digital&Control system are asked in section-B of conventional paper. Four 30 marks questions are asked out of which you have to attempt only two only.

4. Communication system:

This subject can be divided into 2 portion communication-1 which contains AM,FM,PM,PAM,PCM,DM,Match Filter,ASK,PSK,FSK,QPSK,Information theory etc. Communication-2 contains Radar, TV, and Optical Fibre &Satellite communication etc. 12-14 objective questions are asked form communication-1&6-8 objective questions are asked from communication-2 portion. In objective paper questions are very direct in both portion but in conventional paper questions are very difficult and require very-2 depth knowledge.

Reference books for Communication:

1. Principle of Communication by Taub&Schilling
2. Electronics Communication by Roddy&Coolen
3. Modern Digital&Analog Comm. by B.P.Lathi
4. Comm. System by Simon-Haykins
5. Electronics Communication by Kennedy
6. Senior for Optical Fibre
7. Gulati for TV
8. DC agrawal for satellite

Strategy at Panacea for Communication system:

Our class notes information&concepts are based upon above books&last 15-20 years IES/GATE questions. In communication you can attempt all questions after attending our classes. In RADAR, TV, Satellite&OFC most of the question come from our class notes only.

5. Microwave engineering:

This subject requires very less effort for cracking the questions as syllabus of this subject is very small and is overlapped with last potion of EMT.Waveguide, Antenna&wavepropogation come in EMT as well as in Microwave also. 20 objective and 40 marks conventional questions are asked from this portion. In this portion questions are information based and can be solved very easily.

Reference books for Microwave engineering:

1. Electronics&communication by Kennedy
2. Microwave by Liao
3. Microwave by Kulkarni

Strategy at Panacea for Microwave Engineering:

Our class notes information concepts&information are based upon above books&last 15-20 years questions. Objective questions are generally based upon information given in Kennedy and most of the conventional questions come directly from Liao. Most of the questions come directly from our class notes only. For conventional we give more stress on Liao. 500-600 questions are given as assignment and many problems in class itself. Additional 300-400 questions are also given as assignment.

6. Microprocessor/Computer engineering/Computer architecture:

This is miscellaneous topic and covers many topics like Microprocessor,CAO, C& some questions from Data structure also. 20 questions are asked form this portion in objective and 40 marks question in subjective papers.

Reference books:

1. 8085 Microprocessor by R.A.Gaonkar
2. Microprocessor Interfacing by Douglas V.Hall
3. Microcomputer System 8086 by Liu&Gibson
4. Computer architecture by Moris meno
5. Book in C by Balaguru swamy

Strategy at Panacea for MP/CAO/CE:

For Microprocessor & CAO we conduct detailed classes and discuss each and every topic. But For C, DS&flowchart we rely more on last 15-20 years questions. For C, DS&flow chart we tell tricks for solving problems. We give 500-600 questions as assignment and many in class itself and many additional&new problems for DS,C&Flow chart.

Strategy for (General Ability Paper):

This is very crucial paper because generally engineers are weak in general study and don't score good marks in this paper:

This paper contains 60 questions from GS&60 questions from English

English section:

1. 20 questions from Vocabulary
2. 10 questions from Grammar (error)
3. 10+20 questions form comprehension& sentence ordering.

GS Section:

1. 10 questions from life science
2. 20 questions from history&polity
3. 20 questions from Geography&Genral knowledge
4. 10 questions from current affair

Strategy for English section:

For English vocabulary try to learn high frequency 3500 words of GRE Barron guide. Most of the vocabulary in IES paper comes from GRE Barron only. For error you can follow any grammar book so that you can have the concept of error finding. For sentence ordering and passage no need of much practice in this section if you are attentive in exam you can solve all these questions very easily.

How we at panacea can help for English section:

For learning high frequency words we are arranging classes by experts who will help you to memorise these words by some audio&video visual effects. And will also give many techniques so that student can remember these words very easily. For error finding we are giving around 150-200 grammar rules and most of the questions in exam are based upon these rules only. For passage and sentence ordering we provide many class room tests and those tests are more than sufficient for doing problems in IES exam.

Strategy for General study section:

General study becomes very difficult for an engineer as they don't study history&geography which are major part of General study. For Geograhly you need a mentor because it is very conceptual subject. For history and geography you can follow NCERT books which are the best available material in market. For current affair you can collect notes of any civil service magazine at the time of IAS pre exam.

How we at panacea can help for General-study section:

For GS classes are taken by a person who has qualified IAS exam. He is the best available faculty for GS in India for IES exam. Most of the questions in IES exam come directly from his class notes .He has experinece of 6-7 years teaching in field of IES exam. For current affair section extra classes are taken and we also provide study material for that section. For current affairs we are conducting classes 2 month before exams so that every current issue can be discussed in great detail.

Salient features of Panacea Institute:

“You ask any doubt from anywhere and any time” Other than objective we give emphasis on conventional paper also.

1. We provide error free solution of last 15 years GATE/IES papers with reference.
2. We discuss tricks for solving objective &conventional problems.
3. We conduct weekly topic tests on pattern of IES.
4. We provide topic wise tricks and formula list for quick revision
5. We discuss questions of different varieties in class.
6. Our class notes contain every possible concept and quality questions.
7. Before GATE&IES exam we provide 1500 probable solved questions for quick revision and improving your preparation.
8. We have library facility for our students.

Faculties at PNACEA IES/GATE Institute:

1. Mr.Yogesh Agrawal (IES&GATE Topper with 7+ years teaching exp) He himself has topped most of the exams and has produced thousands of toppers in last 7 years.
2. Mr.Bijendra saini (IES)
3. Mr.Niraj Kumar (IAS exam qualified)
4. Mr.Vivek Gupta (IES, BTech from IIT)
5. Mr.Shadab (MTech from IIT)
6. Mr.Chandra-Prakash (Scientist R&D, electronics)

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RESULT OF PANACEA IES/GATE INSTITUTE:

(The best institute for Electronics&Communication engineering)

We are the best institute for Electronics&Communication engineering. We have the best faculties for Electornics&Communication engineering. In Last batches 100%students qualified in GATE&Various PSU exams. Many students are selected in IES exam including top ranks. Most of the students qualified BSNL-JTO exam.

Some of our star performers in Electronics&Comm.

This is result of our few topper students only. All of our last batches students qualified in GATE &various PSU exams like ONGC, BEL, NTPC, BHEL,MTNL, ISRO,DRDO, BARC, PGCIL etc. All of our students qualified GATE&various PSU exams.

Rakesh Yadav	IES Rank02&BSNL-Rank-01
Rohit Tripathi	IES Rank-07&BSNL-Rank-02
Vivek Gupta	IES Rank-14&BSNL-Rank-28
Vikash soni	IES Rank-15,GATE-99.94%
Abhsihek	IES Rank-03,GATE-99.8%
Tarun	IES Rank-40,GATE-99.84%
Arjun Menon	GATE-99.8%
Lalit Mohan	GATE-AIR-09
Vijay Manohar	GATE-AIR-15
Shyma M	GATE-AIR-07
Ankit Malik	GATE-99.27%
Basant	GATE-99.1%
Dinesh Kumar	IES Rank-04(2007)
Shekhar kumar chaudhary	Engineering Service Exam(IES)
Ramavtar Meena	Engineering Service Exam(IES)
Uttam chand Meena	Engineering Service Exam(IES)
Manoj Kumar	Engineering Service Exam(IES)
Arvind Kumar	Engineering Service Exam(IES)
Amit Kumar	Engineering Service Exam(IES)
Himanshu Jain	IES Rank-09(2007)
Brijesh kushwaha	IES Rank-14(2007)&GATE-99.25%
Ankit	BSNL-Rank-19
Vikash Gupta	BSNL-Rank-43
Aditya prakash	BSNL-Rank-46
Heenika Arora	GATE-98.98%
Shishir	GATE-98.89%

Note: we are the No.1 institute for electronics&Communication engineering which is clear from performance of our students also. Our faculties are permanent and always available for students. We have also given the detailed solution of IES-2009 paper in electronics&communication engineering which you can collect from our office or for soft copy you can mail us at panacea.institute@gmail.com

